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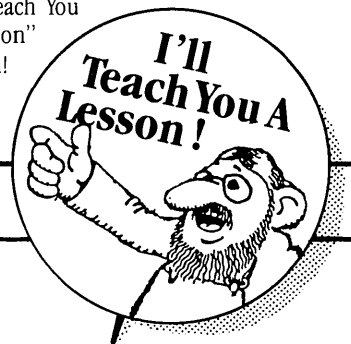
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Table of Contents

- **Defusr** page 2
By Terry Kepner *Q&A*

- **The Data Gatherer** . page 4
By Dennis Kitsz *Part V*

- **Translator** page 13
By Robert L. Hawkins *Fortran to Basic*

- **Hints & Tips** page 17
By Terry Kepner *Good Stuff*

- **On Sig—** page 18
A continuing story

- **Reviews** page 20

I recently heard of a program by Dennis Kitsz that teaches Assembly Language. What are the facts on this? Also, I'm looking for an Epson printer for my 16K CoCo 2 cheap. Any ideas?

P.K.
Plaquemine, LA

You heard correctly. His 6809E lessons are reviewed in the January 1984 issue of *Hot CoCo*. The reviewer, Richard Ramella, gave them a good review and recommended them. The only warning is that you must want to learn to program or you'll become bored and not finish the tapes.

If you live near a large city, check the stores for "want-ad" booklets. These frequently have computer equipment for sale. Also, check the newspapers, local computer stores, and user groups. Someone is sure to have an Epson for sale that you can afford. Just be sure the Epson you get has an RS-232 board installed or it won't be of any use to you.

What are the differences between CSAVE "file-name" and CSAVE "file-name",A? What are the pros and cons of each?

Is there a command to data-files as SKIPF is to programs? If so, what is it?

Cory Sawyer
Lanesboro, MA

The CSAVE "filename",A syntax is used to save a Basic program in ASCII format instead of the normal tokenized format. All the Basic commands, such as Print, INKEY\$, etc., are saved as text, just as you see them on the screen, instead of their one-byte tokenized forms as they are normally stored in memory and on tape (tokenized programs take less tape space and load quicker).

The advantage of the ASCII format is that programs saved in that form can be read by other programs with the normal INPUT#1 statement, as if they were data files. Similarly, programs can be loaded into word processors for manipulation and modification and saved to tape for reading back into Basic.

The disadvantage is that you have to watch that the length of the ASCII Basic program lines aren't longer than 244 characters, or you'll get a DS Error message (direct statement error) when you try to reload the program (a DS Error means you tried to load in ASCII or tokenized characters without there being a line number in front of them).

The only safe way to do it is to save the program normally, then save it in ASCII and reload it. If you get a DS Error message, list the file to find the last line that loaded correctly. Now reload the normal file and list it to the line after that one. The line after the last one to load is the errant line. Edit the line to make it shorter, or break it into two new lines.

Now repeat the procedure until your ASCII file can reload entirely without a DS Error.

No, there isn't a SKIPF equivalent for data files. The only way to skip files is to open the file you want and wait for the tape to cue up to the proper spot on the tape.

I have been using a Color Computer for about two years, primarily for small-business accounting. A few months ago I upgraded my "E" board to Extended Basic and 32K. I used 64K chips but have not gotten around to soldering the wire jumper yet. Since the upgrade, several intermittent problems have occurred and no one seems to be able to help me.

When using the edit command in the insert mode, the cursor will occasionally hang up. It stays in one location, blinks, but doesn't respond to anything but the Reset button. This then requires that I retype nearly the entire (sometimes lengthy and complicated) line. Is this a faulty chip or an incurable problem?

My graphics functions operate sporadically. When trying the sample programs and exercises in the front of the Going Ahead with Extended Color Basic book I often get an irregular oval on each side of the first three demonstration squares, or else a checkerboard pattern covers the top three-quarters of the graphics screen. Sometimes the same programs work fine. Can this problem be fixed?

When using the Complete Personal Accountant package from Futurehouse, the cursor will occasionally disappear for twenty to thirty seconds. No data is lost, but I am concerned that someday it may never come back at all! This happens most frequently when entering a large number of checks but may happen when only a few checks have been entered. Is this a hardware or software problem?

Also, I cannot find out how many variables can be built into a program. The blue sheet in my original owner's manual says "you can now have a full 255 characters in data files without having to worry about losing any information." Does this mean that I can have 255 single-character-named variables each with any numerical amount but only 127 double-character variables (x2) each of any value? If I dimension an array of variables (DIM x(20)) can I then include more than 255 variables? Or does "X(20)" copy five of the 255 allowed?

Roy Hakala
Red Wing, MN

Sounds like you are having problems with your RAM. Get out your 32K conversion instructions and double-check that everything is wired correctly. And finish the job! Anything that's only partially completed cannot be expected to perform correctly.

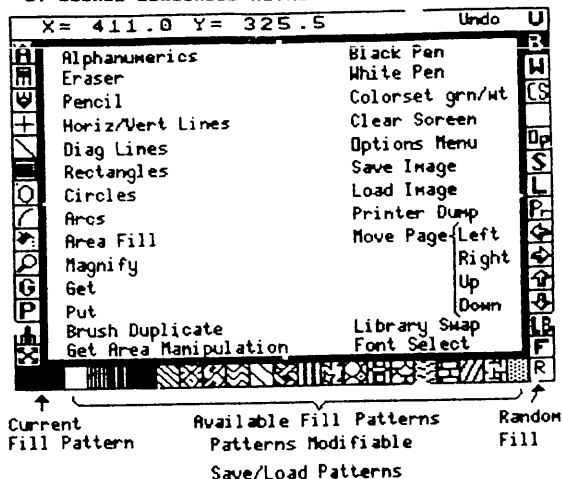
If you are still experiencing problems after that, get the Radio Shack Diagnostic ROMpak and run a memory check. You might have a marginal RAM chip. If you still can't get it to work right, take the unit to the repair center and have them examine it.

The time the cursor is gone is spent in "garbage collection." The computer is busy compacting data in RAM and deleting data no longer in use so that it will have more room for the current data. Don't worry about it.

The reference in the blue sheet to variable length refers to the length of the data that may be crammed into any one variable. The only limit on the number of variables in your computer is the amount of room you have available. You can have two variables or 500, Basic doesn't put a limit on the number, except that imposed by available RAM. If you have the RAM you can dimension an array of X(1000) if you want (or more if you have a small program).

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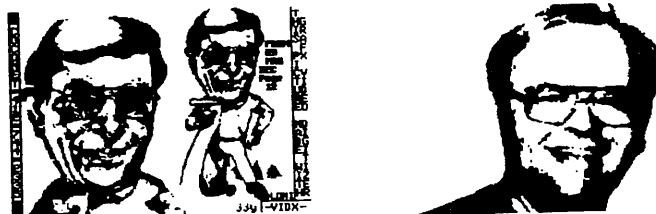
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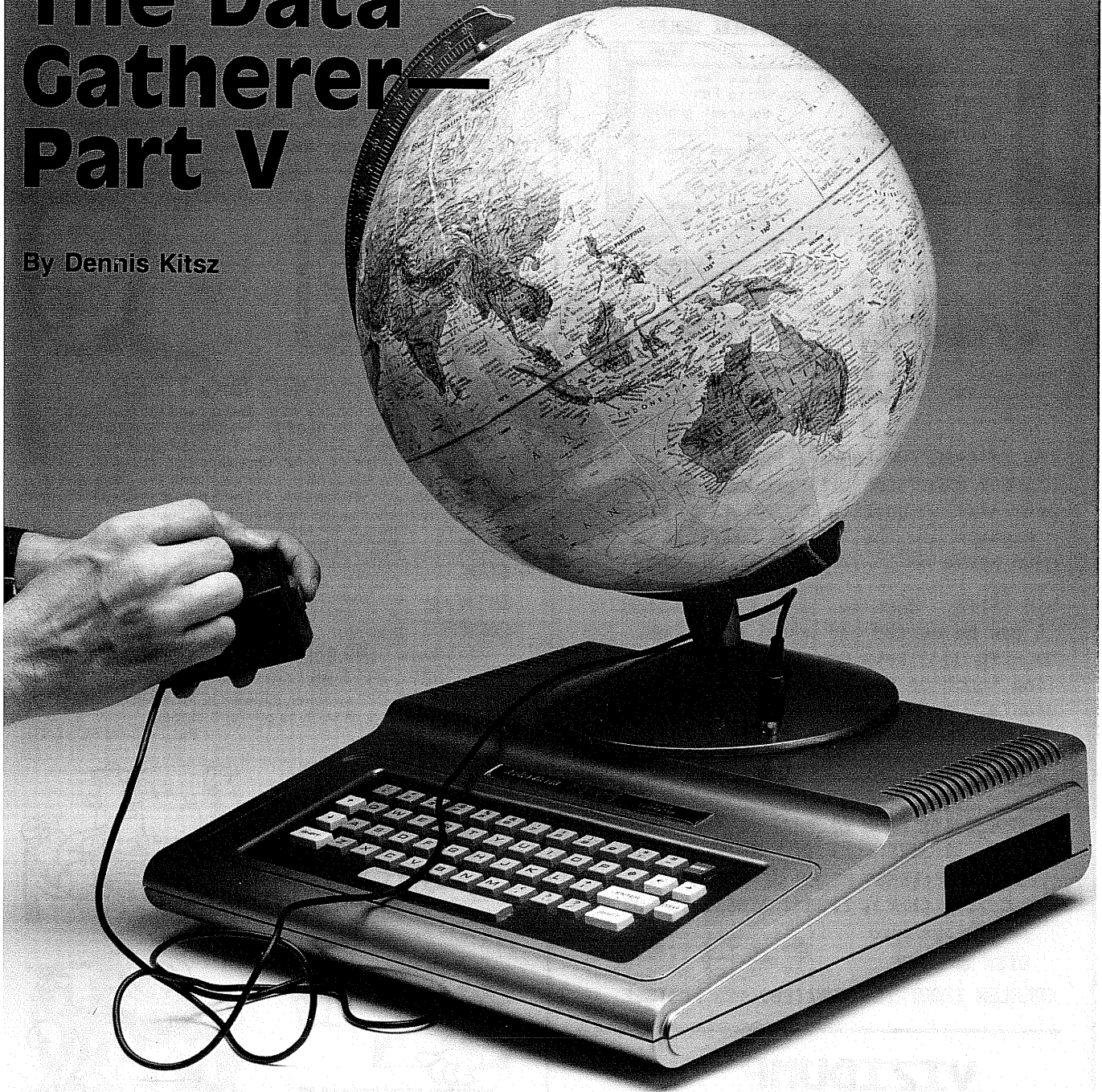
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The Data Gatherer— Part V

By Dennis Kitz



I collect throwaway stuff. Not just any throwaway stuff, but things I can get lots of. Like the centers of Scotch tape rolls. Or old typewriter correction ribbon spools. Magic Marker tops are great. Polaroid instant film batteries are terrific. I even have a crate full of IBM carbon ribbon shells from when I worked as a typist—every one has a wheel, two gears, a spring, and a few other doo-dads! All those things are kept in bags . . . my module bags. When I needed hazardous waste containers to strap to a flatcar for my HO trains . . . there they were: Magic Marker modules! Perfect!

That point of view follows with computer hardware and software. Modularity is one of my favorite approaches. I have small boxes of cassettes, each cassette containing an assembly language module identical to the rest in that box. I pick out the modules I require to assemble long programs.

In the same way, I keep hardware modules around; many of the designs presented in my articles are modular, such as the CoCoPort and the clock/calendar.

No one of the modules is particularly interesting by itself, but the whole result sometimes seems different from those mundane-looking parts. The software for the Data Gatherer follows that pattern, containing about a dozen modules. Each unremarkable module is almost self-sufficient, but in sum they make up an effective, elegant and easy-to-use operating system.

The next two columns will present the complete listing for the Data Gatherer Operating System (DGOS), together with commentary, and a guide to using the software from Basic or assembly language.

Quick Look at the Software

The Data Gatherer Operating System (DGOS) is entirely created in assembly language, and hooks into the Basic language of a 32K Extended Basic Color Computer. DGOS contains initialization (and re-initialization) routines, copyright display, a test for Basic program auto-boot, plus a number of modular segments:

- **Module 1**—Analog-to-Digital Conversion
 - 1a. Opens one analog channel.
 - 1b. Converts one analog value to digital form.
 - 1c. Converts 16 analog values to digital form.
 - **Module 2**—Digital-to-Analog Conversion
 2. Converts one digital value to analog form.
 - **Module 3**—Real-Time Clock/Calendar Control
 - 3a. Displays the real-time clock/calendar (RTCC).
 - 3b. Reads and stores the RTCC.
 - 3c. Sets time and date in the RTCC.
 - 3d. Turns on continuous display of the RTCC.
 - 3e. Turns off continuous display of the RTCC.
 - 3f. Swaps to RTCC port addresses.
 - 3g. Swaps to disk port addresses.
 - **Module 4**—Parallel Printing
 - 4a. Parallel prints one character.
 - 4b. Parallel prints a string of characters.
 - 4c. Parallel prints the video display.
 - **Module 5**—D/A Conversion Alternate
 5. Converts a digital value to analog form (faster method).
 - **Module 6**—A/D Conversion Alternate
 6. Converts an analog value to digital form (faster method).
- (I have to admit that because these routines are modular, in the actual program they aren't in precisely the order shown above.)

Listing 1 presents the first half of DGOS. Lines 200–470 identify the memory locations DGOS will use for its operation, and lines 520–720 are the addresses of each of the DGOS subroutine modules, permitting use of assembly language indirect subroutine jumps. This type of reference table allows updating and expansion of the operating system without grief and pain. (For a full discussion of the merits of operating system vectors, see "Op Sys SIG Discussion" in issue 5).

Vanity

Ever wonder how the disk pack performs an autostart when you plug it in, even if you cut loose the so-called "hard start" connection (pins 7 and 8)? If you back up one more step, how does Color Basic know when the Extended Color Basic ROM has been added to the system?

Here's how. When you turn on the power, Color Basic goes through an initialization routine, setting up parameters, testing and reserving memory, and presenting you with a blank green screen. When Color Basic is finished, it examines memory locations \$8000 and \$8001 (decimal 32768 and 32769) for the values \$45 and \$58. These are the characters EX, for Extended Basic. If Color Basic finds them, it jumps to Extended Basic at \$8002, where a second initialization is performed. When Extended Basic is finished, it looks at locations \$C000 and \$C001 (decimal 49152 and 49153) for values \$44 and \$4B. These are characters DK, for Disk Extended Basic. If Extended Basic finds them, it jumps to Disk Basic at \$C002, where the third initialization is performed. Finally you get a sign-on message and the OK prompt.

So I took advantage of this DK character search business to give DGOS its autostart . . . and, by using a little deceptive assembly language coding, I was also able to indulge my vanity. Not only did I install the skimpy DK, but I installed a full DKITSZ (*Eds. Note: Groan!*). When Extended Basic finds the DK and jumps to \$C002, it tries to execute the remaining characters (I = \$49, T = \$54, S = \$53 and Z = \$5A) which represent the unimportant command sequence ROLA, LSRB, COMB and DECB. That is followed by the real thing, a branch to the actual DGOS initialization (BRA IPL).

The DGOS initialization (IPL, lines 740–830) informs the Basic language operating system that DGOS requires 128 bytes for its use. It does so by changing the values in the "top of memory" pointer, which Basic uses in its clear command; clear is prevented from using memory above \$7F80 (decimal 32640). It sets up the stack and observes the remaining Basic set-up sequence. It then initializes DGOS itself.

The DGOS initialization (lines 1200–1640) sets up clock registers and configures the four 8-bit ports of the Data Gatherer. These ports, found at \$FF50–\$FF5F, are opened, 30 bits are set for output and two bits are set for input. The ports are then closed. A few important values are established immediately (D/A converter channel, high and low D/A converter strobes, printer mask byte, clock screen display, and screen starting location).

The interrupt vector—necessary to provide a continuous clock display on the screen—is "chained" into place. The old vector is used for the sound command, and the clock display vector links itself into the chain of interrupt services. The clock registers are again set, the clock is set up for 24-hour (military) time, and the clock is turned on.

The initialization routine continues with a sign-on message for DGOS, and finally branches to a Basic auto-boot test routine (from line 830). That module is particularly useful by itself, without DGOS, and can auto-start any Basic program into Extended Basic. Next time I'll take a look at that process, which jumps to the Basic ROM at \$80B8—the OK prompt—if no program is found.

The first thing you see when you turn the power on, then, is the green screen, the usual pause during 32K memory testing, and the DGOS sign-on message, with real-time clock running in the upper right corner and the OK prompt in the upper left corner.

Klock Kalendar Kludge

Clocks and calendars may seem regular—all that repetitive ticking and such—but when it comes to programming, they're just a mess. Tenths of seconds are base 10, seconds and minutes are base 60, hours are base 24, weeks are base 7, months are base 12, days are base 365, and leap years are base 4 . . . so to speak. Or so to program.

You run into more of that confusion when the clock is exclusively software, but you can't get out of it even with a hardware real-time clock/calendar (RTCC) such as this. The ridiculous results are found in the display routine (lines 2700–3210).

After the screen display start position is identified the routine moves to the subroutine to read the MM58274 clock/calendar (lines 3290–3450), which in turn moves to the swap-to-clock segment (lines 3500–3540). The swap is required you remember, because the clock must share memory space with the disk. By writing 1 to address \$FF58, the swap flip-flop accesses the clock, and then returns to GETCLK.

After storing the user's registers, the clock values are moved into a storage area pointed to by the Y register (lines 3310–3390). A little peculiarity follows. Naturally, it takes some time to read the clock, even in assembly language. During this time the clock may have begun a change cycle. If that change is at the hour or the day, perhaps, the software will get half of the old time and half of the new time. The result will be a very wrong mixture of time value.

A safeguard is provided, however; it is the "data changed" flag in the RTCC chip. If bit 3 of address 0 in the clock is high, then the clock has been updated . . . so the assembly language program has to re-read the clock for a correct value. Finally it can return to the display routine.

The actual displaying picks up at line 2730, where X is pointed to the screen display position, saved, and re-pointed to the day-of-week value in the clock storage area. Y points to the table of weekdays, beginning with Sunday. The value is plucked from the table and displayed, one character at a time, followed by a space (lines 2820–2920).

The numbers are then pulled from the clock storage area and displayed: the year, a space, the month, a slash, the date, a space, the hour, a colon, the minute, a colon, the second, a period, and the tenths of seconds. Each value (only four bits in size) is stripped of its excess bits and converted for VDG display. (The conversion is unusual because the VDG does not display characters according to their proper ASCII codes. For more on the VDG, refer to *The Color Computer Magazine*, August 1983).

Finally, 166 bytes, 903 machine cycles or 1.01 milliseconds later, the routine is ready to return to the program at hand. And remember of course, that when there is a continuous on-screen clock display, this is an interrupt service routine, adding a shade more time to the process. The overall result is about just under a 5 percent loss of computer speed. *

Out the Converter

The output of a 12-bit value through the digital-to-analog converter is quite fast, suitable for professional sounding single-voice music synthesis or high-speed lab or motor control applications. The routine begins at line 2460.

Recall that the AD667 D/A converter is versatile, capable of interfacing with 4-, 8-, 12- and 16-bit microcomputer systems. It does this by using three independent 4-bit latches preceding a master 12-bit output latch, in addition to a 4-bit control latch. Therefore, the 12-bit data and control word can be installed in many combinations: as four 4-bit nybbles, as two 8-bit bytes, or as a complete 16-bit word.

The Color Computer is an 8-bit system. So at line 2480, 8 bits are placed into the D/A converter, and the remaining four bits—plus four control bits—are stored in the converter at 2510. The strobe routine (2610) pulses the D/A converter to accept the data being made available to it; at lines 2560–2570, those 12 bits are strobed through to the master latch and out the converter.

This is a very orderly subroutine; by sacrificing some of that orderliness, a faster routine can be created (later for this).

Cornering the Prey

There are times when I realize I've got a lot more to learn as a programmer. Writing the subroutine to input an analog value through the Data Gatherer by successive approximation brought that weakness into focus once again. In fact, only an intuitive sense tells me there must be an easier way.

A	B	SX1	SY1	SX2	SY2	Channel Number	Select (Hex)	Channel (Hex)
x	x	0	0	0	0	None	00	—
0	0	0	0	0	1	0	01	00
0	1	0	0	0	1	1	11	01
1	0	0	0	0	1	2	21	02
1	1	0	0	0	1	3	31	03
0	0	0	0	1	0	4	02	04
1	0	0	0	1	0	6	22	06
1	1	0	0	1	0	7	32	07
0	0	0	1	0	0	8	04	08
0	1	0	1	0	0	9	14	09
1	0	0	1	0	0	10	24	0A
1	1	0	1	0	0	11	34	0B
0	0	1	0	0	0	12	08	0C
0	1	1	0	0	0	13	18	0D
1	0	1	0	0	0	14	28	0E
1	1	1	0	0	0	15	38	0F

Table 1. Output Arrangement

Readers? Help!

First, let me tell you what I did (clumsy but successful). The conversion routine begins at line 2030, where a loop is set up: X points to the location where the working offset will be stored, Y points to the location where the 12-bit "known" value will be stored, A accepts the count value and stores it, and D is established as the midpoint between low and high values (midway between 0 and 4095). With these parameters in place, the successive approximation begins at line 2110.

The first value is output through the D/A converter, and the status of the comparator is read (COMPIN) and stored for reference (LASTIN). Remember that the unknown value is discovered by comparing it with a known value, which is what the hardware voltage comparator does.

So, if the hardware voltage comparator reads 1, then the unknown value is greater than the known value at the output of the D/A converter. If the comparator reads 0, then the unknown is less than the known value. ROLA (rotate-left-accumulator, line 2150) forces the value of bit 7 into the carry flag. Based on the contents of the carry flag, the subroutine either adds half again to the known value (2270) or subtracts half again from the known value (2170).

The count is decremented (2340) and the process repeated 12 times until the known value homes in on the unknown value. Finally, depending on the final state of the carry flag, the result is adjusted for known = unknown, against known ► unknown. The final value is stored, and also returned in the D register.

What tells me there must be a better way? Something about the adding and subtracting tells me . . . there's just too much of it, and it feels clumsy. The pattern of bits in the known value seems very regular, but I can't put my finger on it. Do you see it? If so, let me know. Either a free Data Gatherer PC board or a free dinner in Roxbury, VT awaits the first person who can rewrite that routine using bit manipulation—and who can get a single conversion down to 1 mS or less!

And the Rest

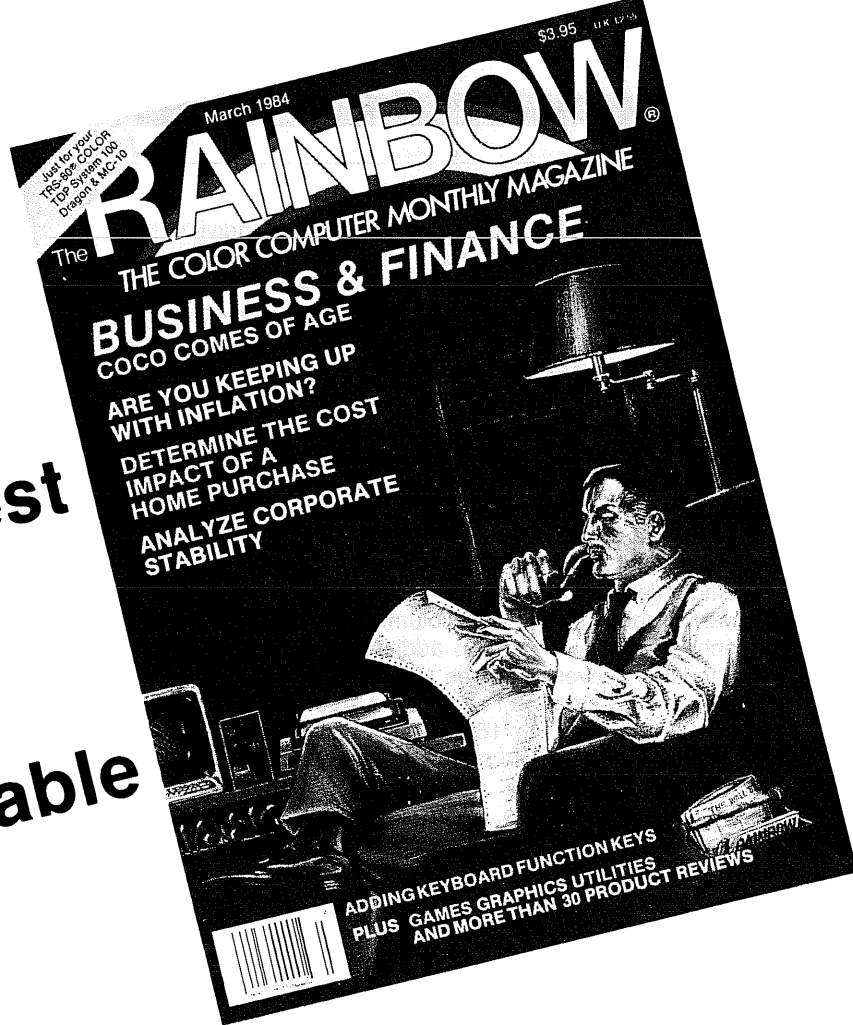
The rest of the A/D conversion section involves turning on the channel to be converted, and converting all 16 channels.

The channel selection routine is messy because of the nature of the analog multiplexer; Table 1 is the arrangement of output again.

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Recall the Basic program used for testing the individual channel input? The Select column in the Table reveals where those mysterious looking data statements were derived from. Depending on the state of the X and Y select lines, the values of A and B determine which input are fed through the analog multiplexers. Notice that only one X or Y select line may be turned on at a time; the software must respect this or risk opening two input at once and feeding input into each other! (The initialization routine establishes channel 1 at power-up, just to be safe.)

The channel routine (line 1810) gets the value from MXCHAN (1 to 16), puts it in the range 0 to 15, and muscles it into the format shown above. Compare the adjusted channel number (last column) with the value needed to select the hardware properly (next-to-last column). You can follow the sequence of logical shifts and additions that perform the rather tedious conversion (lines 1840-1980).

Finally, the loop to select each of the 16 channels in turn is shown beginning at line 1660. This is a simple loop, storing the channel value, and storing the 16 input values in a block of memory.

Next time: Print routines, clock on and off routines, auto-boot Basic from ROM, and application ideas. (end)

Program. Half of The Listing!

```

00100 *****
00110 * The Data Gatherer *
00120 *
00130 * Copyright (c) 1984
00140 * Dennis Bathory Kitsz
00150 * All Rights Reserved.
00160 *
00170 *****
00180 *
00190 *
00200 MVALUE EQU $7F82 * Final 12-bit value
00210 OFFSET EQU $7F84 * Working offset status
00220 VALHI EQU $7F86 * Value to output via DAC
00230 STRSTR EQU $7F88 * Addr. of string to print
00240 SSTART EQU $7F8A * Start of video screen
00250 DISPLC EQU $7F8C * Clock display location
00260 MXCHAN EQU $7F8E * Channel of mux to open
00270 HISTRB EQU $7F8F * Hi DAC strobe pulse
00280 LOSTRB EQU $7F90 * Lo DAC strobe pulse
00290 COUNT EQU $7F91 * Number of output bits
00300 LASTIN EQU $7F92 * Last comparator input
00310 SAVE EQU $7F93 * DAC working area
00320 MASK EQU $7F94 * Printer input mask
00330 STASH EQU $7F95 * Working area for ADC
00340 PRNTC EQU $7F96 * Character to print
00350 INTVEC EQU $7F97 * Interrupt vector

```

00360 *	00370 INTJMP	EQU	\$7F99	* Op sys vector point
7F99	00380 STOR32	EQU	\$7F9B	* 16 2-bytes in from DAC
7F9B	00390 CLKSAB	EQU	\$7FBB	* 14-byte clock store/set
00400 *	00410 *	ORG	\$C000	* First location in ROM
00420 *	00430 CLOCK	EQU	\$FF40	* Clock \$FF40-\$FF4F
FF40	00440 PORT	EQU	\$FF50	* DAC Port \$FF50-\$FF52
FF50	00450 COMPIN	EQU	\$FF54	* Comparator input
FF54	00460 SWAP	EQU	\$FF58	* Disk/clock swap latch
FF58	010D	EQU	\$010D	* Basic interrupt vector
00480 *	00490 AUTO	FCC	'DKITSZ'	* DK = autostart
44				
4B				
49				
54				
53				
5A				
0006 20	00500	BRA	IPL	* Autostart to IPL
00510 *	00520 XIPL	FDB	IPL	* I
C032	00530 XINIT	FDB	INIT	* N
C04B	00540 XREINIT	FDB	REINIT	* D
C00C	00550 XCHANL	FDB	CHANL	* I
C00E	00560 XCONVT	FDB	CONVRT	* R
C010	00570 XGETVL	FDB	GETVAL	* E
C012	00580 XGET16	FDB	GET16	* C
C0F6	00590 XDACOT	FDB	DACOT	* T
C016	00600 XFSTAD	FDB	FASTAD	*
C362	00610 XFSTDA	FDB	FASTDA	* J
C01A	00620 XCLKST	FDB	CLKSET	* U
C01C	00630 XDICKON	FDB	DISCLK	* M
C01E	00640 XCLKON	FDB	CLKON	* P
C020	00650 XCLKOF	FDB	CLKOFF	*
C230	00660 XGETCK	FDB	GETCLK	* V
C281	00670 XPRNT1	FDB	PRINT1	* E
C299	00680 XPRNTS	FDB	PRINTS	* C
C02A	00690 XPRNTV	FDB	PRINTV	* T
C2BF	00700 XSWAPD	FDB	SWAPD	* O
C02C	00710 XSWAPC	FDB	SWAPC	* R
C258	00720 XCPYRT	FDB	COPYRT	* S
C051	00730 *			
C032 8E	00740 IPL	LDX	#\$7F80	* Protected Op Sys memory
C035 9F 74	00750	STX	<\$74	* Maximum system memory
C037 9F 27	00760	STX	<\$27	* Basic high memory
C039 9F 23	00770	STX	<\$23	* Basic high memory
C03B 30 89 FF38	00780	LEAX	-200,X	* Clear 200 bytes
C03F 9F 21	00790		<\$21	* Into clear area
C041 1F 14	00800	TFR	X,S	* Set up stack for Basic
C043 BD AD19	00810	JSR	\$AD19	* Observe Basic setups
C046 8D 03	00820	BSR	INIT	* Set up Op Sys from ROM
C048 16 038D	00830	LBRA	ATEST	* And go to Basic OK
C04B 8D 3D	00840 *			
C04D 8D 02	00850 INT	BSR	REINIT	* Initialize routine first
C04F 39	00860	BSR	COPYRT	* Then copyright message
	00870	RTS		* Back to calling program
C050 39	00880 *			
	00890 STUFF	RTS		* For later expansion

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C051	10BE	7F8A	00900 *	LDY	SSTART	* Get screen starting point	C0CE	FD	7F8C	01500	STD	DISPLC	* And put in reference	
C055	30	8D 03E5	00910 COPYRT	LEAX	SIGNON,PCR	* Point to sign-on	C0D1	CC	0400	01510	LDD	\$0400	* Get normal screen pointer	
C059	31	A8 60	00920	LEAY	\$60,Y	* Move three lines down	C0D4	FD	7F8A	01520	STD	SSTART	* And put in reference	
C05C	8D	01	00940	BSR	VIDEO	* And print message	C0D7	30	8D 0244	01530	LEAX	INTDIS,PCR	* Get int. vector	
C05E	39		00950	RTS		* Back to calling program	C0DB	BF	7F97	01540	STX	INTVEC	* And put in reference	
C05F	A6	84	00960 *	LDA	X	* Get first character	C0DE	17	0177	01550	LBSR	SWAPC	* Swap to clock mode	
C061	81	00	00970 VIDEO	CMPA	\$00	* Is it zero?	C0E1	4F		01560	CLRA		* Set A to zero	
C063	27	24	00980	BEQ	OUTIPL	* If so, end of message	C0E2	B7	FF40	01570	STA	CLOCK	* Make sure ctrl. reg. okay	
C065	81	0D	00990	CMPA	\$00D	* Is it carriage return?	C0E5	B6	FF4F	01580	LDA	CLOCK+15	* Point to set reg.	
C067	27	20	01000	BEQ	OUTIPL	* If so, end of message	C0E8	84	0C	01590	ANDA	\$00C	* Mask in bits 2 & 3	
C069	1F	89	01020	TFR	A,B	* Begin ASCII to VDG conv.	C0EA	8A	01	01600	ORA	\$01	* Set bit 0	
C06B	C4	E0	01030	ANDB	\$0E0		C0EC	B7	FF4F	01610	STA	CLOCK+15	* Clock = 24 hrs	
C06D	27	12	01040	BEQ	JDOIT		C0EF	17	0161	01620	LBSR	SWAPD	* Swap back to disk	
C06F	C1	40	01050	CMPB	\$040		C0F2	17	0205	01630	LBSR	CLKON	* Turn clock on	
C071	27	0E	01060	BEQ	JDOIT		C0F5	39		01640	RTS		* Back to calling program	
C073	C1	60	01070	CMPB	\$060		C0F6	86	10	01650 *	LDA	\$010	* Get 16 channels ready	
C075	26	04	01080	BNE	JNEXT		C0F8	B7	7F8E	01660	GET16	MXCHAN	* Start with channel 16	
C077	84	3F	01090	ANDA	\$03F		C0FB	8E	7F9B	01670	STA	#STOR32	* Point X to storage	
C079	20	06	01100	BRA	JDOIT		C0FE	34	10	01680	LDX	X	* Save storage pointer	
C07B	C1	20	01110	CMPB	\$020		C100	8D	0A	01690	NEXT16	GETVAL	* Get one value from ADC	
C07D	26	02	01120	BNE	JDOIT		C102	35	10	01700	BSR	X	* Get storage pointer	
C07F	8B	40	01130	ADDA	\$040		C104	ED	81	01710	PULS	,X++	* Store value and bump	
C081	A7	A4	01140	JDOIT	Y	* Store char. to screen	C106	7A	7F8E	01720	STD	MXCHAN	* Get next channel	
C083	30	01	01150	LEAX	1,X	* Move to next character	C109	26	F3	01730	DEC	NEXT16	* If not zero, continue	
C085	31	21	01160	LEAY	1,Y	* Move to next screen posn.	C10B	39		01740	BNE		* Back to calling program	
C087	20	D6	01170	BRA	VIDEO	* And repeat till done	C10C	8D	03	01750 *	RTS			
C089	39		01180	OUTIPL	RTS	* Back to calling program	C10E	8D	25	01770	GETVAL	CHANL	* Turn on channel	
C08A	17	01CB	01190 *				C10F	8D		01780	BSR	CONVRT	* Do analog/digital conv.	
C08D	4F		01200	REINIT	LBSR	SWAPC	C110	39		01790	RTS		* Back to calling program	
C08E	B7	FF40	01210	CLRA		* Swap to clock mode	C111	B6	7F8E	01800 *	LDA	MXCHAN	* Get value for channel	
C091	17	01BF	01220	STA	\$FF40	* Set A to zero	C114	4A		01820	DECA		* Strip offset	
C094	8E	FF50	01230	LBSR	SWAPD	* Set clock registers	C115	1F	89	01830	TFR	A,B	* Put into B for work	
C097	86	38	01240	LDX	#PORT	* Swap back to disk	C117	C4	03	01840	ANDB	\$03	* Mask in bits 0 and 1	
C099	A7	01	01250	LDA	\$038	* Point to DAC port	C119	58		01850	LSLB		* And move...	
C09B	A7	03	01260	STA	1,X	* Open port value	C11A	58		01860	LSLB		* ... more ...	
C09D	A7	05	01270	STA	3,X	* Open port \$FF50	C11B	58		01870	LSLB		* ... more ...	
C09F	A7	07	01280	STA	5,X	* Open port \$FF52	C11C	58		01880	LSLB		* ... until LSB is MSB	
COA1	86	FF	01290	STA	7,X	* Open port \$FF54	C11D	F7	7F93	01890	STB	SAVE	* And save for later	
COA3	A7	84	01300	LDA	\$0FF	* Open port \$FF56	C120	44		01900	LSRA		* Divide by two ...	
COA5	A7	02	01310	STA	2,X	* Get all bits output	C121	44		01910	LSRA		* ... and two again	
COA7	A7	06	01320	STA	6,X	* All bits output \$FF50	C122	4C		01920	INCA		* And bump up value	
COA9	86	3F	01330	LDA	\$03F	* All bits output \$FF52	C123	C6	01	01930	LDB	\$01	* Start with bit 0 set	
COAB	A7	04	01350	STA	4,X	* Six bits output	C125	4A		01940	NEXT		* And begin movement	
COAD	86	3C	01360	LDA	\$03C	* Six bits output \$FF56	C126	27	03	01950	BEQ		* If done then mux in place	
COAF	A7	01	01370	STA	\$03C	* Close port value	C128	58		01960	LSLB		* Else move B over one	
COB1	A7	03	01380	STA	1,X	* Close port \$FF54	C129	20	FA	01970	BRA	NEXT	* And do it again	
COB3	A7	05	01390	STA	3,X	* Close port \$FF50	C12B	FB	7F93	01980	GOTIT	SAVE	* B + SAVE = Combination	
COB5	A7	07	01400	STA	5,X	* Close port \$FF52	C12E	FA	7F94	01990	ORB	MASK	* And strip input bits	
COB7	86	01	01410	STA	7,X	* Close port \$FF54	C131	F7	FF54	02000	STB	\$FF54	* And turn on channel	
COB9	B7	7F8E	01420	LDA	\$01	* Close port \$FF56	C134	39		02010	RTS		* Back to calling program	
COBC	86	3C	01430	STA	MXCHAN	* Get channel #1 value	C135	8E	7F84	02020 *	LDX	#OFFSET	* #OFFSET	
COBE	B7	7F8F	01440	LDA	\$03C	* Prepare channel #1 on	C138	108E	7F82	02030	CONVRT	LDY	#VALUE	* S
COC1	86	34	01450	LDA	\$034	* Get DAC high strobe	C13C	86	0C	02040		LDA	\$0C	* U
COC3	B7	7F90	01460	STA	\$034	* And put in reference	C13E	B7	7F91	02050	LDA	STA	COUNT	* C
COC6	86	C0	01470	LDA	LOSTRB	* Get DAC low strobe	C141	CC	0800	02060	STA	LDD	\$0800	* C
COC8	B7	7F94	01480	LDA	\$0C0	* And put in reference	C144	ED	84	02070	STD	.X	* E	
COCB	CC	0409	01490	LDD	MASK	* Get 6-bit print mask	C146	83	0001	02080	SUBD		* E	
					\$04049	* And put in reference				02090		#0001	* S	
						* Get clock screen pointer								

C149 ED	A4	02100	STD	Y, VALHI	S	7F8C	DISCLK	LDD	DISPLC	Get screen display posn.
C14B FD	7F86	02110	AGAIN	STD	I	6F	02710	BSR	GETCLK	* Get clock into memory
C14E 8D	3E	02120	BSR	DAOUT	V	30	02720	PSHS	X,Y	* Stash caller's X & Y
C150 B6	FF54	02130	COMPIN	STA	E	01	02730	TFR	D,X	* Point X to display
C153 B7	7F92	02140	LASTIN	STA	A	10	02740	PSHS	X	* And store it for use
C156 49		02150	ROLA	UGTK	P	7F8B	02750	LDX	#CLKSAV	* Point X to GETCLK result
C157 24	13	02160	BCC	X	P	84	02760	LEAY	DAYS,PCR	* ASCII day table
C159 EC	84	02170	LSRA	X	R		02770	LDA	,X	* Get first GETCLK value
C15B 44		02180	RORB		O		02780	DECA	#S03	* Prepare 1-7 becomes 0-6
C15C 56	84	02190		X	X	03	02790	LDB		* 3 ASCII chars. per day
C15D ED		02200	STD	X	I		02800	MUL	X	* Restore X for use
C15F EC	A4	02210	LDD	Y	M	10	02810	PULS	B,Y	* Display
C161 A3	84	02220	SUBD	X	A	A5	02820	LDA	1,Y	* first
C163 ED	A4	02230	STD	Y	A	21	02830	LEAY	,X+	* character
C165 7A	7F91	02240	DEC	COUNT	T	80	02840	STA	B,Y	* Display
C168 26	E1	02250	BNE	AGAIN	I	A5	02850	LDA	1,Y	* second
C16A 20	11	02260	BRA	DONE	O	21	02860	LEAY	,X+	* character
C16C EC	84	02270	UGTK	X	N	80	02870	STA	B,Y	* Display
C16E 44		02280	LSRA		A	A5	02880	LDA	1,Y	* third
C16F 56		02290	RORB		/	21	02890	LEAY	,X+	* character
C170 ED	84	02300	STD	X	D	80	02900	STA	#S60	* Get VDG space value
C172 EC	A4	02310	LDD	Y		60	02910	LDB	,X+	* And display a space
C174 E3	84	02320	ADDD	X		80	02920	STB	#CLKSAV	* Point to GETCLK result
C176 ED	A4	02330	STD	Y	C		02930	LDV	1,Y	* And bump to next value
C178 7A	7F91	02340	DEC	COUNT	O	21	02940	LEAY	LICK	* Get, mask, display 10Y
C17B 26	CE	02350	BNE	AGAIN	N	33	02950	BSR	LICK	* Get, mask, display 1Y
C17D B6	7F92	02360	DONE	LDA	V	31	02960	STB	,X+	* Another space
C180 49		02370	ROLA	LASTIN	E	80	02970	STB	LICK	* Get, mask, display 10M
C181 25	08	02380	BCE	EXIT	R	2D	02980	BSR	LICK	* Get, mask, display 1M
C183 EC	A4	02390	LDD	Y	S	2B	02990	BSR	LICK	* Get, mask, display 1M
C185 C3	0001	02400	ADDD	#S0001	I	6F	03000	LDA	#S6F	* Get a VDG slash
C188 ED	A4	02410	STD	Y	O	80	03010	STA	,X+	* Display slash
C18A 39		02420	RTS		N	25	03020	BSR	LICK	* Get, mask, display 10D
C18B EC	A4	02430	EXIT	Y		23	03030	STB	LICK	* Get, mask, display 1D
C18D 39		02440	RTS			80	03040	STB	,X+	* Display another slash
C18E FC	7F86	02450	*			A0	03050	LDA	,Y+	* Get next character
C191 34	10	02470	DAOUT	VALHI	Get value to send	03	03060	ANDA	#S03	* Mask AM/PM indicator
C193 8E	FF50	02480	LDX	X	Save X from caller	03	03070	ADDA	#S70	* Mask for VDG
C196 E7	84	02490	STB	#PORT	Point to DAC output	70	03080	STA	,X+	* And display 10H
C198 8A	80	02500	ORA	X	Put LSB into DAC	80	03090	BSR	LICK	* Get, mask, display 1H
C19A A7	02	02510	STA	#S80	Set to strobe 3 nybbles	7A	03100	LDB	#S7A	* Get a colon
C19C 8D	0D	02520	BSR	2,X	And values are ready	80	03110	STB	,X+	* And display it
C19E 8A	F0	02530	ORA	STROBE	Strobe them into DAC	11	03120	BSR	LICK	* Get, mask, display 10M
C1A0 A7	02	02540	STA	#SFO	Set all bits high	0F	03130	BSR	LICK	* Get, mask display 1M
C1A2 84	7F	02550	ANDA	2,X	And put them in place	80	03140	STB	,X+	* Display another colon
C1A4 A7	02	02560	STA	#S7F	Ready to do conversion	0B	03150	BSR	LICK	* Get, mask, display 10S
C1A6 8D	03	02570	BSR	2,X	Put convert command in	09	03160	BSR	LICK	* Get, mask, display 1S
C1A8 35	10	02580	PULS	STROBE	And strobe through DAC	6E	03170	LDA	#S6E	* Get a VDG period
C1AA 39		02590	RTS	X	Restore caller's X	80	03180	STA	,X+	* And display it
C1AB 34	06	02600	PSHS	A,B	Back to calling program	03	03190	BSR	LICK	* Get, mask, display 1/10S
C1AD B6	7F8F	02610	LDA	HISTRB	Save caller's A & B	30	03200	PULS	X,Y	* Restore caller's X & Y
C1B0 A7	01	02620	STA	1,X	Get high strobe value		03210	RTS		* Back to calling program
C1B2 F6	7F90	02630	LDB	LOSTRB	Strobe DAC high	A0	03220	LDA	,Y+	* Get value from clock
C1B5 E7	01	02650	STB	1,X	Get low strobe value	0F	03240	ANDA	#S0F	* Strip off high nybble
C1B7 A7	01	02660	STA	1,X	Strobe DAC low	70	03250	ADDA	#S70	* Mask for VDG display
C1B9 35	06	02670	PULS	A,B	Strobe DAC high	80	03260	STA	,X+	* Display the value
C1BB 39		02680	RTS		Restore caller's A & B		03270	RTS		* Back to calling program
		02690	*		Back to calling program	26	03280	*		
							03290	GETCLK	SWAPC	* Swap to clock mode

C232 34	36	PSHS	X,Y,D	* Stash some registers
C234 8E	FF40	LDX	#CLOCK	* Point to clock port
C237 108E	7FBB	LDY	#CLKSAV	* Point to storage area
C23B A6	84	LDA	,X	* Reset "data changed" flag
C23D C6	0E	LDB	,\$0E	* Number of registers
C23F A6	85	LDA	B,X	* Get \$0Eth register
C241 84	0F	ANDA	,\$0F	* Mask in low nybble
C243 A7	A0	STA	,Y+	* Put into storage
C245 5A		DFCB		* Done with all registers?
C246 26	F7	BNE	REDO2	* If not, then get next
C248 A6	84	LDA	,X	* Read "data changed" flag
C24A 84	08	ANDA	,\$08	* Mask in "d.c." bit
C24C 26	E6	BNE	REDO1	* If data changed, do again
C24E 35	36	PULS	X,Y,D	* Restore the stuff
C250 8D	01	BSR	SWAPD	* Swap to disk mode
C252 39		RTS		* Back to calling program
C253 34	02	PSHS	A	* Save caller's A
C255 4F		CLRA		* Set A to zero (disk mode)
C256 20	04	BRA	SWAPX	* Store in swap flip-flop
C258 34	02	PSHS	A	* Store caller's A
C25A 86	01	LDA	,\$01	* Get A=1 (clock mode)
C25C B7	FF58	STA	,\$FF58	* Store in swap flip-flop
C25F 35	02	PULS	A	* Restore caller's A
C261 39		RTS		* Back to calling program
C262 8D	F4	BSR	SWAPC	* Swap to clock mode
C264 34	36	PSHS	X,Y,D	* Store caller's stuff
C266 8E	FF40	LDX	#CLOCK	* Point to clock registers
C269 108E	7F88	LDY	STRSTR	* Point to setting address
C26D C6	0E	LDB	,\$0E	* Number of clock registers
C26F A6	A0	LDA	,Y+	* Get first new value
C271 A7	85	STA	B,X	* Put into clock register
C273 5A		DFCB		* Decrement register count
C274 26	F9	BNE	REDO4	* Go back for next
C276 A6	84	LDA	,X	* Else read "data changed" flag
C278 84	08	ANDA	,\$08	* Mask in "d.c." bit
C27A 26	EA	BNE	REDO3	* If changed, do it again
C27C 35	36	PULS	X,Y,D	* Restore caller's stuff
C27E 8D	D3	BSR	SWAPD	* Swap back to disk mode
C280 39		RTS		* Back to calling program

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Translator

By Robert L. Hawkins

Fortran was the first of the high-level computer languages. It was designed primarily for scientific and mathematical computing, and in these areas a huge library of useful subroutines is available. The routines may be found in statistics, numerical methods, and data analysis textbooks, and as part of mainframe and minicomputer on-line libraries. Many routines, especially those written in the days when mainframe computers had the speed and memory size of current-day micros (or less!), were lovingly crafted for efficient use of limited memory, and are ideal for microcomputer use. The subroutines would have been written in Fortran II (up to about 1963)

or, in most cases, Fortran IV (the standard version, from 1963 to about 1977).

An option for folks who can't implement Fortran is to translate the subroutines into Basic. Listing 1 performs the most tedious and error-prone parts of the job automatically. It takes a Fortran routine saved as an ASCII file as input, and adds Basic line numbers, translates logical and arithmetic If statements and GOTO statements, and transforms Do loops into For-Next loops. It translates some Fortran keywords and function names, and flags other statements for further translation (I/O statements, subroutine calls). The translated routine is written out as a Basic program in ASCII format.

Running

Create the original ASCII file with a word processor or by downloading the Fortran routine from a mainframe or minicomputer using a modem. The file should be only one routine; Fortran line numbers may be repeated in different routines.

The listing will ask for a five-digit line number to begin with, and Basic line numbering will increase in steps of 10 from this point.

The Fortran source will then load from cassette and be translated. You may send the translated program to tape or the printer, or just view it on the screen.

Listing 2 is the translated output of the sample Fortran source, provided for example.

The translated Basic version will be saved to tape. You will probably have to complete the translation process manually. Some Fortran statements are simply untranslatable: for example, there is no call subroutine in Basic. You will have to replace calls with GOSUBs, and resolve any variable name conflicts between the calling and the called routine. I/O statements will be flagged but not translated. Most Fortran programs assume that output goes to a printer with a 132 column width; most write statements will have to be rewritten for this reason, anyway. In any case, it is not considered good programming practice to have I/O statements in mathematical subroutines, so the problem may not occur.

Some untranslatable keywords, such as implicit, equivalence, and so on, will be ignored by Translator. Since they always occur at the beginning of a routine, they are easy to find. Some—especially equivalence—may render the entire routine untranslatable, because their effects may not be duplicatable in Basic. You have to have a pretty good knowledge of Fortran to handle the more difficult jobs.

Less common constructs, such as the seldom-seen assigned GOTO, and the Fortran 77 extensions, such as the elusive block if, are not translated. Older routines generally don't use such constructs—in fact, many don't even use the logical if, sticking strictly to the arithmetic version (translated to a Basic On . . . GOTO). Variable typing also is ignored, since Basic doesn't have double-precision or integer variables, and strings are rarely used in Fortran. Be sure to read through the Fortran source for potential problems.

Even if no untranslated keyword occurs, you are likely to have problems with variable names. Any variable name that starts with a Basic reserved word is illegal. Thus, FNAME is perfectly legal in Fortran, but not in Basic, since FN is reserved. Also TIME1 and TIME2 are different variables in Fortran, but the same to your Color Computer, since Basic looks only at the first two letters. It is useful to use a variable name cross-reference generator, which will find all the variable names in a program and list them alphabetically.

You can use the built-in Basic line editor to make the necessary changes, but a word processor or programming utility makes it much easier. Instead of changing each variable name individually, you can use a global search/change function.

Also watch out for Fortran integer variables (those which start with letters I through N, unless explicitly declared otherwise) being set equal to real variables or expressions. You should use the Basic fix function on the real expression to chop off the fractional part.

The speed or size of the translated program can be improved with a little more editing. For example, in most cases,

an arithmetic If can be reduced to a simple If . . . Then statement, rather than the On . . . GOTO produced by Translator.

To add Fortran keywords to Translator's vocabulary, check the data statement in line 1320. Words to be flagged but not translated should be added to the beginning of the data statement. Also, add one to both of the numbers in line 1300 (total keywords and untranslated keywords). Keywords to be translated should be added to the end of line 1320, and only the first number in line 1300 increased. In both cases, the flag or the translation should go into the corresponding position in line 1340. Translation of constructs which need more than straight one-for-one replacement will have to be handled individually.

The translator is written for 32K. If you have 16K, you should remove all remarks and unnecessary spaces, change line 90 to 90 CLEAR 6000, and reduce NMAX in line 1100 to 200. Before CLOADing the Translator, PCLEAR0 (POKE 25,6 : NEW) to open up maximum memory.

In spite of the exceptions, the great majority of Fortran IV statements will be successfully translated. (end)

```

C TEST FILE FOR FORTOBAS
  SUBROUTINE STATS (Y, N, AVG, STDDEV, IERR)
C DIM STATEMENT SHOULD BE REMOVED FOR BASIC:
  DIMENSION Y(1)
  IER=0
  AVG=0.0
  STDDEV=0.0
C
C MAKE SURE N IS GREATER THAN ZERO:
  IF (N) 999, 999, 100
  100 DO 200 I = 1, N
    AVG=AVG+Y(I)
    STDDEV=STDDEV + Y(I)**2
  200 CONTINUE
C
C "FLOAT" ISN'T NEEDED IN BASIC:
  AVG=AVG/FLOAT(N)
  STDDEV = SQRT(STDDEV/FLOAT(N) - AVG ** 2)
  GO TO 1000
C
C ERROR IN INPUT DATA:
  999 IER=1
  IF (N .EQ. 0) GOTO 950
C N LESS THAN ZERO:
  WRITE (6,601) N
  601 FORMAT (1X, 'ERROR -- N = ', I5, ', LESS THAN
    0')
  GO TO 1000
C
C N = ZERO:
  950 WRITE (6, 600) N
  600 FORMAT (1X, 'ERROR -- N WAS ZERO')
  1000 RETURN
C
C #####
C SOME DELIBERATE ERRORS:
C UNDEFINED LINE NUMBERS:
  GOTO 3000
  DO 3000 K=1, 5, 2
C MISSING THIRD NUMBER:
  IF (N) 999, 999
C #####

```

Figure 1. Sample Fortran Source

```

10 '*****
20 'FORTRAN-TO-BASIC TRANSLATOR'
30 '
40 '   ROBERT L. HAWKINS
41 ' 1286 HUNTER AVE., APT. C
42 '   P.O. BOX 8162
43 ' COLUMBUS, OHIO 43201
44 '*****
80 CLS 4 : PRINT "  fortran to basic translator" :
PRINT
90 CLEAR 16000
994 '   **=
995 '*initialize*'
1000 TRUE=-1 : FALSE=0 : IO=-1 'IO=1 FOR DISK
1005 'LOGICAL-VALUED FUNCTIONS:
1010 DEFFN CM(X)=(LEFT$(A$,1)="C") 'COMMENT?
1020 DEFFN CON(X)=(MID$(A$,6,1)<>" ") 'CONTINUATION
?
1030 DEFFN UM(X)=((D$>="0"AND D$<="9") OR D$=" ") 'N
UMERIC OR BLANK?
1094 '   **=
1095 'PROGRAM IS STORED IN Q$(0..NL).
1096 '
1100 NMAX=600 : DIM Q$(NMAX)
1110 NL=0
1115 '   **=
1120 INPUT "  STARTING LINE NUMBER >= 10000"; LN :
IF LN<10000 THEN SOUND 2,2 : GOTO 1120 'STARTING BAS
IC LINE#
1195 'LOGICAL OPERATIONS:
1200 DATA 8
1210 READ NOP
1220 DIM FOP$(NOP), BOP$(NOP)
1225 'FORTRAN OPS:
1230 DATA .LT.,.GT.,.EQ.,.LE.,.GE.,.NE.,.AND.,.OR.,
.NOT.
1235 'BASIC OPS:
1240 DATA <,>,<=,>=,<>," AND "," OR "," NOT "
1250 FOR I=0 TO NOP : READ FOP$(I) : NEXT I
1260 FOR I=0 TO NOP : READ BOP$(I) : NEXT I
1293 '   **=
1295 'FORTRAN TO BASIC KEYWORDS:
1300 DATA 17, 10
1310 READ NKEY, NSKIP '0..NSKIP ABORT FURTHER TRANS
LATION
1315 'FORTRAN:
1320 DATA " READ("," READ("," WRITE("," WRITE(","
FORMAT("," FORMAT("," CALL EXIT("," CALL("," SUBR
OUTINE("," DIMENSION("," CONTINUE "," SQR("," SQR(","
ALOG("," ALOG("," ATAN("," ATAN("," ***"
1335 'BASIC:
1340 DATA " 'read("," 'read("," 'write("," 'write(
"," 'format("," 'format("," STOP "," SUBGO "," <
"," DIM("," : "," SQR("," SQR("," LOG("," LOG("," ATN(
"," ATN("," ~"
1360 DIM FK$(NKEY), BK$(NKEY)
1370 FOR I=0 TO NKEY : READ FK$(I) : NEXT I
1380 FOR I=0 TO NKEY : READ BK$(I) : NEXT I
1993 '
1994 '   **=   **=   **=
1995 '*read FORTRAN INTO Q$(1..NL)*'
2000 PRINT
2010 INPUT "FORTRAN FILENAME"; NM$
2020 Q$(0)="C PROGRAM "+NM$
2030 PRINT : PRINT " OPENING FILE"
2040 OPEN "I",#IO,NM$
2045 'INPUT LOOP:
2050 LINEINPUT#IO,A$
2055 'CONTINUATION CARD? IF SO, CONCATENATE:
2060 IF (FNCON(0) AND NOT FNCM(0)) THEN Q$(NL)=Q$(N
L)+RIGHT$(A$,LEN(A$)-6) : GOTO 2090
2065 'NO -- A NEW LINE:
2070 PRINT Q$(NL)
2075 IF NL<NMAX THEN NL=NL+1 ELSE SOUND 2,2 : PRINT
"TOO MANY LINES --" : INPUT " <ENTER> TO CONTINUE";
Z$ : GOTO 3000

```

```

2080 Q$(NL)=A$
2090 IF NOT EOF(IO) THEN 2050
2100 PRINT Q$(NL)
2110 CLOSE#IO : SOUND 100,2
2120 CLS 5
2125 '   **=
2995 '*add basic line#'s*(Q$(0..NL)
3000 FOR N=0 TO NL
3010 A$=Q$(N) : LA=LEN(A$)
3015 'COMMENT?
3020 IF FNCM(0) THEN C$="REM "+RIGHT$(A$,LA-1) : GO
TO 3060
3025 'SPLIT FORTRAN LINE# (B$) FROM STATEMENT (C$):

3030 C1=5 : C2=7 : GOSUB 9050
3040 LL=VAL(B$) 'LINE# FIELD
3045 'PUT FORTRAN LINE# AT END OF LINE AS REMARK:
3050 IF LL THEN C$=C$+" '"+STR$(LL)
3055 'THE "5" ASSUMES 5-DIGIT BASIC LINE#'S
3060 Q$(N)=RIGHT$(STR$(LN),5)+" "+C$
3070 LN=LN+10 'INCREMENT BASIC LINE NUMBER
3080 PRINT Q$(N)
3090 NEXT N
3100 CLS 3
3993 '
3994 '   **=   **=   **=
3995 '*translate*(Q$(0..NL); Q$(0..NL))
3997 '
4000 FOR N=0 TO NL
4010 A$=Q$(N) : LA=LEN(A$)
4020 IF MID$(A$,7,3)="REM" THEN 4200 'REMARKS UNCH
ANGED
4022 '   **=
4025 'TRANSLATE KEYWORDS:
4030 FOR I=0 TO NKEY : C1=INSTR(6,A$,FK$(I)) : IF C
1=0 THEN 4050 'SEARCH FOR KEYWORD
4040 C2=C1+LEN(FK$(I)) : C1=C1-1 : Z$=BK$(I) : GOSU
B 9000 : IF I<=NSKIP THEN 4200 'REPLACE WITH BASIC

4050 NEXT I
4052 '   **=
4053 '"DO", "IF" AND "GO TO":
4055 '"DO" STATEMENT?
4060 COL=INSTR(6,A$," DO ")
4070 IF COL THEN GOSUB 5000 : GOTO 4200 'DO-TO-FOR
XLATOR
4075 '"IF" STATEMENT?
4080 COL=INSTR(6,A$," IF ") : IF COL=0 THEN COL=INS
TR(6,A$," IF ")
4090 IF COL THEN GOTO 6000 '"IF" XLATOR
4095 '"GOTO"?
4100 COL=INSTR(6,A$," GO ") : IF COL=0 THEN COL=INST
R(6,A$," GO ")
4110 IF COL=0 THEN 4200
4120 COL=COL+3
4130 IF MID$(A$,COL,3)="TO " THEN COL=COL+3 ELSE IF
MID$(A$,COL,4)=" TO " THEN COL=COL+4 ELSE COL=0
4140 IF COL=0 THEN 4200
4150 GOSUB 7000 'GOTO XLATOR
4200 PRINT A$
4210 Q$(N)=A$
4220 NEXT N
4990 GOTO 8000 'FINAL OUTPUT
4992 '
4993 '   **=
4995 '*do-to-for XLATOR*(A$,LA; A$,LA)
5000 C1=COL : C2=COL+3
5010 Z$="FOR" : GOSUB 9000 'REPLACE
5015 'GET DO-LOOP TARGET LINE:
5020 FL=VAL(RIGHT$(A$,LA-COL-3))
5025 'EXCISE IT:
5030 C1=COL+4 : C2=C1+1
5040 D$=MID$(A$,C2,1) 'SEARCH FOR NON-NUMERIC,NON-B
LANK
5050 IF FNUM(0) THEN C2=C2+1 ELSE 5100
5060 IF C2<LA THEN 5040 'KEEP LOOKING

```

```

5065 'IF YOU GET HERE, ERROR:
5070 MSG$="LOST IN DO"
5080 GOSUB 8500
5090 RETURN
5095 'FOUND -- D$ IS FIRST CHAR OF DO VARIABLE NAME
:
5100 DV$=D$
5110 C3=C2+1
5120 D$=MID$(A$,C3,1)
5130 IF D$="" THEN 5170 'END OF VARNAME
5140 DV$=DV$+D$ 'BUILD VARNAME
5150 C3=C3+1 : IF C3>LA THEN 5070 'LOST
5160 GOTO 5120
5165 'EXCISE DO-NUMBER:
5170 Z$="" : GOSUB 9000
5175 'CHANGE "," TO "TO":
5180 C1=INSTR(C1+1,A$,"")
5190 IF C1=0 THEN 5070 'NO COMMA -- ERROR
5200 C1=C1-1 : C2=C1+2
5210 Z$=" TO " : GOSUB 9000 'REPLACE
5215 'IF ANOTHER " , " => "STEP":
5220 C1=INSTR(C1+3,A$,"")
5230 IF C1=0 THEN 5300
5240 C1=C1-1 : C2=C1+2
5250 Z$=" STEP " : GOSUB 9000
5295 'NOW FIX TARGET LINE:
5300 SV$=A$
5310 GOSUB 7500 'FIND BL FROM FL
5320 IF EFLAG THEN MSG$="DO-LOOP TARGET "+STR$(FL)
+" NOT FOUND" : GOSUB 8500 : RETURN
5330 A$=Q$(I) : LA=LEN(A$) 'TARGET LINE
5335 'CHECK IF PREVIOUS "NEXT" HAS BEEN ADDED:
5340 C1=INSTR(6,A$," : NEXT ") : IF (C1<COL AND C1)
THEN COL=C1
5350 C1=COL : C2=COL+1
5360 Z$=" : NEXT "+DV$+" " : GOSUB 9000
5370 Q$(I)=A$
5380 A$=SV$ : LA=LEN(A$)
5390 RETURN
5995 '*if xlator*(A$,LA,COL; A$,LA) "IF (EXPR) STA
TEMENT"
6000 C9=COL : C1=INSTR(COL,A$,"(")
6010 IF C1=0 THEN 6900 'ERROR
6015 'FIND MATCHING PARENS:
6020 C3=1
6030 FOR C4=C1+1 TO LA
6040 D$=MID$(A$,C4,1)
6050 IF D$=")" THEN C3=C3-1 ELSE IF D$="(" THEN C3=
C3+1
6060 IF C3=0 THEN 6100
6070 NEXT C4
6075 'IF YOU GET HERE,ERROR:
6080 MSG$="MISMATCHED PARENS IN IF" : GOSUB 8500
6090 RETURN
6095 'C3,C4 BRACKET (EXPR):
6100 C3=C1
6115 'IF LOGICAL OPS USED, ASSUME LOGICAL IF:
6120 LFLAG=FALSE
6130 FOR I=0 TO NOP
6140 COL=INSTR(C3,A$,FOP$(I))
6150 IF COL=0 OR COL>C4 THEN 6200
6160 C1=COL-1 : C2=COL+LEN(FOP$(I))
6170 Z$=BOP$(I) : GOSUB 9000 'REPLACE
6180 C4=C4+LEN(Z$)-LEN(FOP$(I)) : LFLAG=TRUE
6190 GOTO 6140
6200 NEXT I
6205 'LOGICAL OR ARITHMETIC?
6210 IF LFLAG THEN C1=C4 : C2=C1+1 : Z$="THEN " : G
OSUB 9000 : GOTO 4100 'LOGICAL
6215 'ARITHMETIC: "IF (NUMBER) 100,200,300"
6220 C1=C9 : C2=C1+3
6230 Z$="ON 2+SGN" : GOSUB 9000 'REPLACE "IF"
6240 C4=C4+LEN(Z$)-2 'CLOSING PARENS
6250 C1=C4 : C2=C1+1
6260 Z$=" GOTO " : GOSUB 9000 'INSERT "ON ... GOTO"
6265 'CHANGE TO BASIC LINE#'S:

```

```

6270 C5=C4+LEN(Z$)
6280 C2=INSTR(C4,A$,"") : IF C2=0 THEN 6900
6290 GOSUB 7010 'REPLACE LINE#
6295 'GET NEXT #:
6300 C4=INSTR(C5,A$,"") : IF C4=0 THEN 6900
6310 C5=C4 : C4=C4+1
6320 C2=INSTR(C4,A$,"") : IF C2=0 THEN 6900
6330 GOSUB 7010
6335 'LAST ONE:
6340 C4=INSTR(C4,A$,"") : IF C4=0 THEN 6900
6350 C5=C4
6360 FOR C2=C4+1 TO LA : D$=MID$(A$,C2,1) : IF FNUM
(0) THEN NEXT C2 ELSE C2=C2-1 : GOTO 6380
6370 C2=LA+1
6380 GOSUB 7010
6390 GOTO 4200
6895 '*if error*
6900 MSG$="LOST IN IF" : GOSUB 8500
6910 GOTO 4200
6995 '*goto xlator*(N,COL,A$; A$,LA)
7000 C5=COL-1 : C2=LA+1 'SKIP OVER " GO TO"
7010 FL=VAL(RIGHT$(A$,LA-C5)) 'TARGET FORTRAN LINE#

7020 GOSUB 7500 'FIND BL CORRESPONDING TO FL
7025 'IF LINE NOT FOUND, ERROR:
7030 IF NOT EFLAG THEN 7100
7040 MSG$="LINE# "+STR$(FL)+" NOT FOUND"
7050 GOSUB 8500 'ERROR ROUTINE
7060 RETURN
7095 'TARGET LINE FOUND:
7100 C1=C5 'C2 SET EARLIER OR BY CALLING ROUTINE
7110 Z$=STR$(BL) : GOSUB 9000 'REPLACE LINE#
7120 RETURN
7495 '*find target line*(FL; BL, EFLAG)
7500 EFLAG=TRUE : Z$=" "+STR$(FL) : LZ=LEN(Z$) 'TAR
GET STRING
7510 FOR I=1 TO NL
7520 COL=INSTR(6,Q$(I),Z$) : IF COL<>LEN(Q$(I))-LZ+
1 THEN COL=0 'DON'T CONFUSE "1000" FOR "100"!
7530 IF COL THEN EFLAG=FALSE : BL=VAL(Q$(I)) : RETU
RN 'FOUND
7540 NEXT I
7550 RETURN
7995 '*output*(Q$(0..NL))
8000 CLS : PRINT " TRANSLATED OUTPUT" : PRINT
: IF IO>0 THEN IO$="disk" : C$="SDPQ" ELSE IO$="ca
SSETTE" : C$="SCPQ"
8010 PRINT"OUTPUT TO:" : PRINT
8020 PRINT" sCREEN ONLY" : PRINT
8030 PRINT" ";IO$ : PRINT
8040 PRINT" pRINTER" : PRINT
8045 PRINT" qUIT" : PRINT
8050 PRINT : PRINT "YOUR CHOICE?";
8060 Z$=INKEY$
8070 Z$=INKEY$ : IF Z$="" THEN 8070
8080 I=INSTR(1,C$,Z$) : IF I=0 THEN SOUND 2,2 : GOT
O 8000
8090 ON I GOTO 8100, 8200, 8300 : CLS : PRINTTAB(8)
"== FINISHED ==": END
8095 'SCREEN:
8100 PRINT
8110 FOR I=0 TO NL
8120 PRINT Q$(I)
8130 NEXT I
8140 PRINT : INPUT "<enter> FOR MENU"; Z$
8150 GOTO 8000
8195 'DISK/CASSETTE:
8200 CLS : PRINT IO$; : INPUT " FILENAME"; NM$
8210 PRINT
8220 OPEN"O", #IO, NM$
8230 FOR I=0 TO NL
8240 PRINT#IO, Q$(I) : PRINT Q$(I)
8250 NEXT I
8260 CLOSE#IO
8270 GOTO 8140
8295 'PRINTER:
8300 CLS

```


ROBERT HAWKINS

```

8310 FOR I=0 TO NL
8320 PRINT#-2, Q$(I) : PRINTQ$(I)
8330 NEXT I
8340 GOTO 8140
8350 '*****'
8495 'error MESSAGE*'
8500 SOUND2,2
8510 A$=A$+'*****error****'+EMSG$ : LA=LEN(A$)
8520 PRINT A$
8530 RETURN
8995 'insert Z$ INTO A$*(A$,LA,Z$,C1,C2; A$,LA)
9000 GOSUB 9050 : GOSUB 9100
9010 RETURN
9045 'split A$*(A$,LA,C1,C2; B$,C$)
9050 IF C1<1 THEN B$="" ELSE B$=LEFT$(A$,C1)
9060 IF C2>LA THEN C$="" ELSE C$=RIGHT$(A$,LA-C2+1)

9070 RETURN
9095 're-form A$*(B$,Z$,C$; A$,LA)
9100 A$=B$+Z$+C$ : LA=LEN(A$)
9110 RETURN

```

Listing 2. Translated Program Sample

```

10000 REM PROGRAM FORTEST
10010 REM TEST FILE FOR FORTOBAS
10020 SUBROUTINE STATS (Y, N, AVG, STDDEV, IERR)
10030 REM DIM STATEMENT SHOULD BE REMOVED FOR BASI
C:
10040 DIM Y(1)
10050 IER=0
10060 AVG=0.0
10070 STDDEV=0.0
10080 REM
10090 REM MAKE SURE N IS GREATER THAN ZERO:
10100 ON 2+SGN (N) GOTO 10220, 10220, 10110
10110 FOR I = 1 TO N ' 100
10120 AVG=AVG+Y(I)
10130 STDDEV=STDDEV + Y(I)^2
10140 : NEXT I ' 200
10150 REM
10160 REM "FLOAT" ISN'T NEEDED IN BASIC:
10170 AVG=AVG/FLOAT(N)
10180 STDDEV = SQR(STDDEV/FLOAT(N) - AVG ^ 2)
10190 GO TO 10320
10200 REM
10210 REM ERROR IN INPUT DATA:
10220 IER=1 ' 999
10230 IF (N = 0) THEN GOTO 10300
10240 REM N LESS THAN ZERO:
10250 'write(6,601) N
10260 'format(1X, 'ERROR -- N = ', I5, ', LESS THAN
0') ' 601
10270 GO TO 10320
10280 REM
10290 REM N = ZERO:
10300 'write(6, 600) N ' 950
10310 'format(1X, 'ERROR -- N WAS ZERO') ' 600
10320 RETURN ' 1000
10330 REM
10340 REM #####
10350 REM SOME DELIBERATE ERRORS:
10360 REM UNDEFINED LINE NUMBERS:
10370 GOTO 3000'*****error**** LINE# 3000 NOT FOUND

10380 FOR K=1 TO 5 STEP 2'*****error**** DO-LOOP T
ARGET 3000 NOT FOUND
10390 REM MISSING THIRD NUMBER:
10400 ON 2+SGN (N) GOTO 10220, 999 '*****error****
LOST IN IF
10410 REM #####

```

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☆ **Fm: Roger Bouchard** (to A. Flexser)

Once again, what in heaven is wrong with the RAM hook? If need be you can use a disk routine by calling it by its command if you use the disk command table of pointers!

(A reply follows:)

Fm: A. Flexser (to R. Bouchard)

Most disk commands do not have RAM hooks attached that are useful entry points from ML programs.

To elaborate . . . I do agree that the use of the command dispatch table (via the RAM entry point at \$137 is a technique that should be used when possible in preference to the more usual technique of checking for version number and using one of two possible jump addresses depending on version. But, the problem is that, more often than not, one does not want to jump into the ROM to

a point that corresponds to the first byte of a Basic command routine. Usually, one wants to skip over a scan for arguments, or use a subroutine that is also used by one or more Basic commands. The point is that more of these general-purpose entry points should have been allotted their own (ROM) vectors.

★ ★ **Fm: Marty** (to J. Ross)

True, John, but the point of having an operating system in the first place is to *not* force the programmer to write his or her own disk I/O, file handling, keyboard, screen, etc., routines. The solution you mentioned will work, but the fact that folks have to resort to it speaks of the abysmal lack of planning by Microsoft and Tandy in their miserable R/S DOS.

(The one answer follows:)

Fm: John Ross (to Marty)

I agree totally! But, since RS and Microsoft did not see fit to supply constant vector tables, and insist on moving things around, that approach is the *only* one that makes any sense! Otherwise you spend all your time changing versions for new ROMs, and answering hate mail!

☆☆ **Fm: A. Flexser** (to Marty)

Yes, introducing those vectors in a "better late than never" fashion would be advantageous in limiting the number of versions future software might have to check for. Plus, if the same vectors were in the 09, it might lead to transportability between new CoCo software and that machine.

(One reply follows:)

Fm: Marty (to A. Flexser)

Very good point. I didn't mention that.

But that point is probably the single most cogent argument for their bothering at all to introduce disk file I/O vectors at this late date. Yes, very good point indeed. Now let's see what ol' Wayne can answer to that one! (Boy, do I ever get annoyed by folks who apologize for the blunders of others!)

☆ ☆ **Fm: Wayne Day** (to Marty)

Simply because I'm tired of hearing people complain about the way it is . . .

Tandy, again and for the last time, told folks from the very beginning to not use the un-documented calls . . . and those programmers who have made life very miserable for a lot of otherwise innocent folks.

I'm interested mainly in the average non-techie user's viewpoint here . . . not someone who *knows* how it's "supposed" to be done, but rather the person that plunks down \$25 to \$100 for a program and then finds it doesn't work on their machine.

Again, I stand by my original statement, and it is one you can never change my mind about (at least until the hardware addresses change): The majority of compatibility problems existing in the CoCo today are because of programmers who were lazy, or inefficient, or simply unable to write their own code so that their product would be ROM-version independent.

You, I, nor any of the people on this Sig will *never* change Tandy's mind about declaring all ROM calls, or at least the vectors, to be sacred.

(One reply follows:)

Fm: Marty (to W. Day)

Guess you were serious in what you were writing, not just playing devil's advocate. Sorry to hear that. You are simply, totally, plumb dead wrong in the position you have taken. That's all there is to it, and it's really not much of a matter of opinion. I'd venture to guess that 95 percent or more of assembly language programmers for the CoCo would agree with me. If you've ever written or began to write a machine language program that did disk I/O of whole files on the CoCo and under other operating systems, you'd understand. Well, you're generally so right on most CoCo and computer-related matters that I guess you can afford the luxury of being totally, laughably wrong in this one area.

Be well.

(One reply follows:)

Fm: John Ross (to Marty)

I don't think Wayne is saying he agrees with their method, he is just saying that Tandy *warned* them, and they should have listened! Those that did not, have suffered for it, as have the people that buy their programs.

(OK folks. That's the end of that conversation thread. Now, think back a few sections:)

Fm: Marty (to S. Trevor)

What you said has merit, but if you look at the variety of customized disk ROMs on the market, you'll find that virtually all of them leave DSKCON, its vectors, and the disk file handling code intact in the same places that it was in the R/S DOS parent ROM. That's why I suggested checking for version type by looking at the DSKCON vector. I grant you that if programmers were not so vain and did not mess with the version number byte, things would be better. But most programmers *are* vain! Also, vanity aside, what about this possibility? Tandy comes out with a new disk ROM version number, but the file-handling routines are in the same place. In such an event, your program would give up and die with a gasp of "alien ROM!," whereas one checked for vectors would go merrily on its way. Perhaps a more rigorous version type check would consist of checking both the DSKCON vector and the first two bytes of the entry points to those subroutines that the program was to use. If those checked out, regardless of other mods, the odds would be good that it would have a friendly operating environment. What do you think of that idea? It's cumbersome, of course, but it might not be a bad idea.

(A reply follows:)

Fm: Sandy Trevor (to Marty)

OK, we'll consider modifying the version check.

Fm: A. Flexser (to S. Trevor)

Sandy, I've got to agree with Marty on this. So what if a version check of DSKCON results in Vidtex mistakenly thinking that it is dealing with (say) RS-DOS 1.0 when it is in fact dealing with a modified third-party ROM. If that modified ROM kept DSKCON in the

same place as the RS-DOS ROM, chances are Vidtex will work OK. And if not, it'll just crash, which is exactly what it does now if that logo byte isn't in the right place.

(A reply follows:)

Fm: Sandy Trevor (to A. Flexser)

It shouldn't crash! It should work fine but without the disk commands. What Disk ROM causes Vidtex to crash?

(A reply follows:)

Fm: A. Flexser (to S. Trevor)

Anything (like JDOS, or ADOS in its initial versions) that doesn't have that logo byte where Vidtex expects it to be. It just locks up, as I recall. (Wayne, is that your recollection from the early ADOS?)

(A reply follows:)

Fm: Mike Ward (to A. Flexser)

I wonder why nobody seems to have any trouble with the Mouse? (Clara runs it on a CoCo II with 1.1 Disk ROMs.) You use ADOS and I clunk along with my old 1.0 ROMs . . .

(Two replies follow:)

Fm: Wayne Day (to M. Ward)

It *obviously* couldn't be superior programming! hehehe

(The reply is:)

Fm: Mike Ward (to W. Day)

Undoubtedly true! They're merely flabbergasted!

Fm: A. Flexser (to M. Ward)

Because you took the "lazy" way and used undocumented ROM calls, you layabout, you!

(The reply:)

Fm: Mike Ward (to A. Flexser)

That's true, and I also was careful to see which version of the ROMs are installed. If I can't figure it out, I assume 1.0.

(The reply:)

Fm: A. Flexser (to M. Ward)

Sounds good to me! (end)

Disk Drive Controllers: A Comparison

By Martin Goodman, M.D.

PRODUCT

CoCo II Disk Controller
Radio Shack
\$90-\$130

J&M Controller
J&M Systems
Albuquerque, NM
\$120-\$140

HDS Disk Controller
HDS

Since the introduction of the Color Computer there have been at least seven different Color Computer Floppy Disk Drive Controller boards available to the public. Of these, three have enjoyed extremely wide-scale sales, and two of those three are still in production. I'm going to concentrate on the two popular ones currently being sold, but I'll mention others.

What is a Floppy Disk Controller? It's that black box that plugs into the side of the CoCo (or into your system bus) at one end, and through a cable to your disk drive unit(s) at the other. In the case of the CoCo disk controller, this item is a circuit board that contains both the hardware needed for the computer to talk to the disk drive (disk controller chip and numerous small scale logic support chips) and the added software (usually Disk Extended Color Basic) needed to drive the hardware. That software is actually "firmware"—a ROM or EPROM chip. It's important to remember that while you often buy the two (disk operating system software and hardware) together, in a CoCo disk controller they are two separate items. That is, one can plug a different ROM or EPROM into the disk controller and have either Disk Extended Color Basic, JDOS, Spectrum Dos, ADOS or any of a number of different variant Disk Basic operating systems.

Warning: While a disk controller is meant to connect to its disk drives through a ribbon cable, it is *not* designed to connect to the computer through a ribbon cable. While a number of third party suppliers sell "Disk Extender Cables" and "Y cables," I urge you *not* to use such products! Both the lack of proper grounding and the added capacitance of the ribbon cable on the unbuffered CoCo's system bus will, on many systems, cause intermittent system crashes (typically during disk drive use). This is a lesson I've

learned through bitter experience and extensive testing and polling of my fellow CoCo users. Heed this warning!

With one trivial exception, to be mentioned at the end of this article, all CoCo disk controllers are designed to hook up to any 35- or 40-track single-sided double-density disk drive. With appropriate exotic operating systems they can be all made to operate, to some degree or other, with double sided and/or 80-track drives, but such operation is for experienced system hackers only. Most CoCo users should stick with the Tandy standard, of 40-track single-sided double-density drives. I myself take this advice. Experienced OS-9 users and BBS system operators may be exceptions.

- Tandy was the first to introduce a double density disk controller for the Color Computer. This, the CoCo I disk controller, was the only game in town for quite a while. It worked reasonably well, even though in some respects its design was a bit clumsy. I've been using a CoCo I disk controller on my main CoCo system for the last two years, and it has been trouble free. My criticisms of the unit relate to the fact that some of them tended to pop their 74LS02 chips now and then, and that they sometimes required adjustments of the potentiometers on the oscillator for the data separator.

There is another problem with the old CoCo I disk controllers: when Tandy stopped making the old D, E, and NC (F) boards, and switched over to making the Color Computer 2, they altered the computer so it no longer provides +12 volts on its system bus. The CoCo I disk controller required +12 volts to operate its archaic Western Digital 1793 chip. So unless you modify your CoCo 2 (articles have been written about this) you can't use the old CoCo I controller directly with the new CoCo 2. If you are using a Tandy or PBJ multiple port bus, you will be able to use a CoCo I controller with a CoCo 2.

The CoCo I disk controller is no longer sold. Tandy has discontinued it in favor of its CoCo 2 disk controller. The CoCo 2 disk controller represents a distinct improvement over its predecessor. Its circuitry is cleaner and requires less frequent adjustments; because Tandy switched over to the Fujitsu MB8877A disk control chip, it does not require +12 volts. This means it works *equally* well on both the older and the newer model Color Computers. It is available from Tandy National parts, if you order the circuit board and the two halves of the plastic shell. Cost will be between \$90 and \$130. Overall, the CoCo 2 disk controller is a very reliable, well-designed and -built unit. I have one very serious criticism of it: despite insistent pleas from knowledgeable users and hackers over the last five years (this stems from the days of the Model I) Tandy still has *not* plated the connectors of its disk controller with gold. There is abundant evidence that the failure of Tandy to do so has resulted in system crash after awhile, due to oxidation of the contacts on the disk controller. A partial remedy consists of periodically removing the disk controller card from its shell and gently cleaning its contacts with a soft pencil eraser. I've "repaired" at least three Radio Shack store computers in just such a fashion.

The permanent fix consists of either sending the

unit out to a qualified electrical gold-plating establishment (hard to find) or soldering on a product called the Gold Plug. This item is sold by EAP Corporation, POB 14, Keller, TX 76248, (817)498-4242. It requires a little hardware ability to install, but once installed it will fully cure problems relating to oxidation of the contacts. Cost of the package is around \$18.

I should note in passing that Tandy, in a display of sublime technical stupidity, has recently started releasing ROMpaks with gold-plated connectors (ROMpaks don't really need gold-plated connectors) but still does not gold plate the connectors of the disk controller or the Multipak, both of which do need gold plating.

- By far the best known and most widely sold non-Tandy CoCo disk controller is made by J&M Systems of Albuquerque, NM. This unit came on the market just before the arrival of the Tandy CoCo 2 disk controller. It features competent design, a sturdy metal case, and gold-plated connectors. This last makes it a very desirable unit. I and many friends have used the J&M Systems controller extensively and have generally been very pleased with its reliability and ruggedness. The circuit board is well laid-out and manufactured in a high quality fashion. Like the CoCo 2 disk controller, it features a quartz crystal-controlled phase-locked loop data separator that requires no adjustment.

When friends of mine managed to blow out their unit (by unplugging it from the computer while the system was on) J&M provided extremely prompt repair services. Overall, it's a very nice unit, superior in some ways to the Tandy product, and I recommend it. It retails for around \$120 to \$140. I have a few complaints and cautions about the J&M product: J&M gave me a run-around when I tried to order a schematic from them. For months they stalled me . . . first refusing to send me one, then claiming they were in the process of printing a service manual. After about a half-dozen calls over a four month period (and a letter), I finally received a badly Xeroxed copy of the schematic and some other technical info, including spec sheets on the Synertec chip they use in their controller. They charged me \$25 for those 10 pages.

The J&M controller uses a Synertec disk controller chip which is very similar to, but slightly different from, the Fujitsu chip used in the Tandy CoCo 2 disk controller. While this will make no difference in running normal software, some weird copy protection schemes may result in disks that won't load on systems with the J&M system controller, and some disk clone programs may not work properly with that controller. Also, the Synertec chip is much harder for the hacker to find.

The J&M disk controller is sold with a choice of disk operating systems on ROM or EPROM. You can order it with Tandy's Disk Extended Basic or with JDOS, an "enhanced" Disk Basic. *Don't* buy it with JDOS: order it with Tandy's standard Disk Basic. If you do not heed this warning you'll discover a number of valuable pieces of software that simply won't run right under JDOS. While JDOS is allegedly "compatible" with Tandy's Disk Extended Basic, don't believe it!*

- HDS Disk Controller: HDS (formerly Compukit) is advertising a disk controller of its own design. This unit will be available as an assembled and tested unit, as a kit, and as a bare-board with instructions. The company apparently has a sensible policy of making documentation on their product freely available. I had a chance to play with one of these units; it featured not one, but two 24-pin ROM sockets, and a jumper for selecting one ROM or the other. This makes it possible to quickly switch between two different operating systems (say JDOS and Tandy's Disk Extended Basic). This feature is of moderate interest to hardware hackers, but in my opinion virtually worthless to the average user. Unfortunately, both sockets are 24-pin. It would have been much better had HDS offered one of the two sockets as a 28-pin socket to fit 2764 EPROMs, which are much cheaper and more available than the 24-pin 68764's that the unit current requires.

I briefly tested the unit and it appeared to function well. While I did not have a schematic, the circuit seemed very similar to that of the Tandy CoCo 2 disk controller. I then proceeded to modify the board to accommodate a 28-pin 2764 in one of the slots so its owner could use both 2764 and 27128 EPROMs in it. In the course of doing this, I discovered the printed circuit board to be made in a significantly inferior fashion to that of the J&M and Tandy boards. It lacks lacquer and is much more easily damaged. The traces on the board seemed thinner and more fragile than those on the Tandy and J&M boards.

I'd previously modified CoCo I, CoCo 2 and J&M disk controller cards to take 28-pin sockets. Modifying this one proved five times harder, due to the flimsy construction of the board. Therefore, my first impression of this unit is that one should *not* buy it unless one is an inveterate kit builder (it *is* the only unit offered as a kit). The assembled unit may also be of interest to hackers, because unlike both the Tandy and J&M units, HDS sockets all chips on the board.*

- Modifying a controller for 28-pin sockets: CoCo 2 disk controllers are about the easiest to modify for 28-pin sockets, with J&M and CoCo I controllers just a little more difficult. The technique I use requires a 28-pin AUGAT-style socket. I clip off the thin part of pins 1, 2, 28, 27 and 26 socket, leaving the fat part of the pin under the socket as something to solder to and as a physical spacer under the socket. I then prepare each socket by soldering on 30 gauge wires as needed to these "stumps" of pins. I then desolder the 24-pin socket, place tape on top of the area where the socket was, make needed trace cuts on the board, solder in the modified 28-pin socket, then hook up loose ends, including running a wire from pin 26 of the socket to land 37 (A13 line) on the CoCo port edge-connector.

When modifying J&M controllers you'll have to fuss with the +5 volt supply, as there's a trace you must cut but also jumper to a further point. You'll see what I mean if you look carefully at it and if you know what you're doing.

The whole process is a bit delicate but not all that difficult. It takes me about an hour. I've done it about half a dozen times now. Pin-outs for the 68764 and

27128 are, of course, essential. There are about three pins that need to be reshuffled. As an added hint, I'll tell you that pins 1, 27, and 28 of the 28-pin socket are wired to each other and to the +5 line. Pin 2 of the 28-pin socket is connected to the trace that formerly went to pin 18 of the old 24-pin socket. Pin 23 of the 28-pin socket is connected to the trace that formerly went to pin 18 of the 24-pin socket. Pin 20 of the 28-pin socket goes to ground. Pin 26 of the 28-pin socket is jumpered to land 37 (A13) on the CoCo bus edge-connector. The 28-pin socket is inserted into the holes for the 24-pin socket so that pin 3 and 26 of the 28-pin socket line up with pins 1 and 24 of the holes for the 24-pin socket.

All unmentioned pins connect straight through . . . no further jumpers needed.

- Other units: I've seen or heard of three other disk controller units for the CoCo. One features a 28-pin socket so you can jumper to accept either 24- or 28-pin EPROMs. Another is from DSS (formerly Saturn). And if I'm not mistaken, Star Kits was once marketing a disk controller, though it may have been one of the above two. Both units seemed to work acceptably, but I really can't recommend either, if only because they're odd-ball items that offer little if anything in either price or performance over the more widely distributed controllers. Please note I'm not saying they're any worse than the more commonly sold units—merely that they did not appear to be to be significantly enough better to warrant buying an obscure item.

Hard-core CoCo trivia buffs will be amused to learn that the late Exatron Corp. briefly marketed a *single-density* disk controller and operating system for the CoCo before Tandy came out with its system. The only example of such a unit in captivity that I

know of is at the home of Bob Rosen. Indeed, I viewed the other two less well-known CoCo disk controllers at Bob Rosen's "Museum of Weird and Obscure CoCo Hardware."

- Conclusions: there you have it. At least seven different disk controllers were designed and produced for the Color Computer. But only two are in current production *and* in wide circulation. Deciding between these two will be difficult. The J&M unit is a bit superior to the Tandy unit, and comparably priced. I would prefer that unit because of its gold-plated connectors. But the Tandy unit is not bad, especially if you solder on EAP Corp. Gold Plugs to it. Hackers will be disappointed to hear that all small-scale logic chips on both the CoCo 2 (Tandy) and the J&M disk controller are soldered, not socketed. Tandy repair is accessible around the country via your local R/S store. This may influence some to stick with the Tandy product; but as I write, the somewhat superior J&M product is serviced promptly and conscientiously by mail by J&M. I've several positive reports from satisfied J&M customers who needed repairs and no negative ones. (end)

**Since this review was written, both HDS and J&M have issued new controllers. Both use the new Western Digital Controller, a 28-pin second generation disk controller. All contacts on both of these controllers are now gold-plated. The J&M controller now supports 2764 and 27128 EPROMs; the new HDS controller supports 2764, 27128 and 27256 EPROMs.*

Space Shuttle

By Jeffrey S. Parker

PRODUCT

Space Shuttle

By John Frayesse

Tom Mix Software

4285 Bradford N.E.

Grand Rapids, MI 49506

(616) 957-0444

Requires 32K Extended Color Basic

\$28.95 Tape

\$31.95 Disk

"Houston, this is Discovery, we have achieved altitude and are on course. Velocity 12000."

"Roger, Discovery, we copy. Telemetered data is 5-10-10 on the plot board. Standby for main booster shutdown at 53000."

"10-4, Houston, that's a go. We have orbit. Satellite on tracking, Houston. My God, Houston, the stars sure are bright out here!"

If you have ever seen the pictures of the space shuttle in the magazines, or ever watched a launch on tv, then you have probably wondered what it would really be like to fly the Space Shuttle. Now, John Frayesse, a software author for Tom Mix Software, has given *you* that opportunity, right in your own living room, on your Color Computer.

The Space Shuttle simulator is just that, a simulation, not a toy or a game. While it doesn't cost the millions and millions of dollars that NASA's cost, and it will not enable you to walk into NASA and present your credentials as an astronaut, it will give you a good sense of what it is like to fly the space shuttle.

This program probably pushes the Color Computer to the very limits of its 32K capacity. The

graphics display incorporates high resolution moving graphics and text. The display screen is usually running four separate data displays at the same time. These graphic displays are some of the most sophisticated and detailed displays created for the Color Computer.

The documentation to fly your simulator provides you with a mission plan in five parts, and guides you in ten carefully detailed pages through each of the phases of the flight plan necessary to successfully complete your mission: Phase 1 is Launch, in which you must achieve a specific orbit and course direction; Phase 2 is Park, in which you must successfully locate and place the shuttle in a "parking orbit" near a damaged communications satellite; Phase 3 is Fetch, in which you must open the shuttle bay doors, manipulate the robot arm to capture the satellite, bring it safely back into the shuttle, and close the bay doors; Phase 4 is Entry, the most difficult part of the simulation, in which you must guide the shuttle to a course, altitude, speed, and target destination; Phase 5 is Final Approach, in which the shuttle is now a supersonic glider, that you must glide safely to the runway, fighting crosswinds to make a precise touchdown.

If all these tasks are not enough of a challenge, the whole mission is flown against a time clock and a fuel gauge. You are given the weather conditions and the runway heading at the beginning of every flight, and these coordinates are different each time you fly. Are you still sure you want to be an astronaut?

The flight controls, like the screen displays, are intricate. The keyboard and one joystick are used, but the joystick has a different orientation for each phase of the mission. For example, the joystick controls the main engines during launch (phase 1), but when the satellite is being retrieved during phase 3, the joystick controls the robot arm.

The instrument panel is also complex, constantly changing during the flight to bring you the essential data you need regarding course, velocity, speed, and altitude. In addition, you are given a plotting scope, similar to radar, and event labels, which change as you proceed from one phase or subphase of the mission to the next. You are provided with fuel consumption data, a mission clock, a shuttle mode indicator, (to indicate which engines are on), and a reaction jet console to tell you which of the steering motors is being fired to move the shuttle in a given direction. Extra data as to runway heading and the range to the runway is provided during the landing phase. The view out the window of the shuttle also changes from phase to phase, showing you the launch, as you move through the clouds into space, the parking "windows" to approach the satellite, the view of the shuttle aft, as you watch the doors open and the arm operating, and finally, after blackout on reentry, the runway landing scene.

Space Shuttle is a very sophisticated demonstration of what the Color Computer can do. The program provides hours of challenge and fun, and some learning as well, as to what it takes to fly a real space rocket. Some of the best features of the program

are its high resolution graphics and sound effects. The moving graphics display, and the multifunction instrument display, are some of the finest uses of animated graphics I have seen for this machine; they are a real pleasure to experience. The boredom or "shelf" factor of Space Shuttle is extremely low, because no two flights are alike, so the simulator does not lose its challenge even after repeated use. The debriefing, in which you receive your flight time, score, and other landing or ("sigh") crash data after every mission is excellent in helping you track your flight failures and successes.

There are only three weaknesses that I found in flying the Space Shuttle Simulator. The first is the use of the reset button to control the color calibration. The graphics are very nice, but having to reset and then run the program as much as twenty or more times before beginning can be frustrating indeed. A good deal of calibration is necessary because blue and red can become interchanged due to the use of multicolored high resolution graphics.

The second is the Fetch phase of the flight. This segment is used to give the pilot a guaranteed score of at least 100 points. If you fail before and after the Fetch, the shuttle computer puts the ship on automatic, and you are penalized points and time and aborted to the next phase of the flight. The Fetch phase does not have that option; the satellite must be retrieved to continue, and there is no abort. Retrieving the satellite is a relatively simple feat, and can become boring and repetitious. This step could be made more challenging by adding a timer, or by repositioning the satellite slightly differently each time, rather than in one given spot to the left or right of the shuttle.

Last but not least, in the actual Park phase, where you must put the shuttle in an orbit window near the satellite, you are required to use the up or down arrows on the keyboard to accelerate or decelerate to match orbits with the satellite. The keyboard read routine in this part of the simulation is very slow, and sometimes you must wait several seconds to push the key again before the computer will scan and accept the command. This can cause you to fly right by the satellite, and abort this phase of the mission. A more rapid keyboard scanning here could help this problem, especially since the machine is busy with so many other simultaneous calculations.

All in all, Space Shuttle is an entertaining and rewarding program, with some very accurate and realistic details. While this is not a game, and I do not recommend it for young children or those unacquainted with other flight simulations, Space Shuttle provides hours of satisfaction to anyone who enjoys meeting difficult challenges. The displays and moving graphics are finely detailed, realistic and accurate, and are a pleasure to experience.

One final note is that the product is guaranteed to load for one year from purchase. As it happened, the copy I received on disk did not load. Tom Mix Software was both courteous and extremely quick to replace my copy. It is not only a pleasure to use the Space Shuttle Simulator, it is a pleasure to do business with such a responsive company. (end)

Trivia

By Mark Haverstock

PRODUCT

Trivia Fever

Professional Software, Inc.

P.O. Box 533

Needham, MA 02194

64K Extended Basic

\$29.95 Disk

Q: What is gray or white, has 53 keys and plays trivia games?

A: Your Color Computer.

Trivia Fever is a computerized trivia game that provides a more random selection of questions than its manual counterparts, as well as some desirable automatic features. Up to eight players or teams may participate. A question book and tally sheets are also provided for non-computerized play.

Under its blue and gray cover lies a slickly executed OS-9 program. However, there's no need to buy an OS-9 operating system; side one of the disk includes an OS-9 boot and system disk with game instructions. On the reverse side reside the question files.

Complete instructions are given for running Trivia Fever on Disk Basic 1.0 and 1.1. On the newer 1.1 Disk Basic, typing dos and the enter key will load and execute the program. Using Disk Basic 1.0, there are two alternatives. If you presently own the OS-9 operating system, merely insert the boot disk and run it. Otherwise you must create your own boot disk using the 21 line program described in the documentation: such a disk could just as easily have been provided by the manufacturer as a convenience to the user. Troubleshooting hints are enclosed in case the program does not load properly. My sample copy showed no hesitation in loading.

Instructions for playing Trivia Fever can be ac-

cessed by typing y at the first prompt. If you want to review parts of the rules, these may be restarted by pressing i. Once the Trivia Master is chosen and the individuals or teams entered, the disk should be turned over and reinserted. At this point, play begins.

There are several features in Trivia Fever that enhance game play. A built-in timer counts down the amount of time a contestant has left to answer a question. Current scores can be handicapped in three different ways: 1) by the number of points necessary to complete a category; 2) random selection of question categories; and 3) by the amount of time given a specific player to answer a question.

The object of the game is to successfully complete questions in each of five subject categories. Questions may be chosen from three levels of difficulty. Once the level is chosen, the computer picks a random query. If a question repeats itself during the contest, it may be scratched by the Trivia Master and another can be chosen. Witty little remarks are made after each question is answered, such as "Don't get too excited, everybody gets that one."

Overall, Trivia Fever appears to be well organized and has clear instructions. It shares enough similar traits with another famous trivia game that an experienced player should be able to proceed with minimal instruction. The self-scoring timing and handicapping are desirable features because they handle some of the game's tedious tasks automatically. Still, the human intervention of the Trivia Master is important, as he or she becomes the moderator and final judge of the game events.

Looking beyond the basic package, there seem to be enough additional question disks available to keep a trivia buff occupied for quite awhile. An enclosed order blank lists ten more disks of trivia questions that can be ordered from PSI for \$24.95 each. Volumes 2-4 are currently available. Topical disks include Trivia Fever Super Sports, Trivia Fever Entertainment, and Trivia What's in a Word, available at \$39.95. Multiple Learning Disks, listed as "to be announced," contain various questions in academic areas. Perhaps parents and teachers will be able to cash in on the trivia game popularity for reviewing and memorizing facts.

I have one suggestion for PSI: make a do-it-yourself system disk that would allow the student, teacher or parent to enter their own categories and questions. (end)

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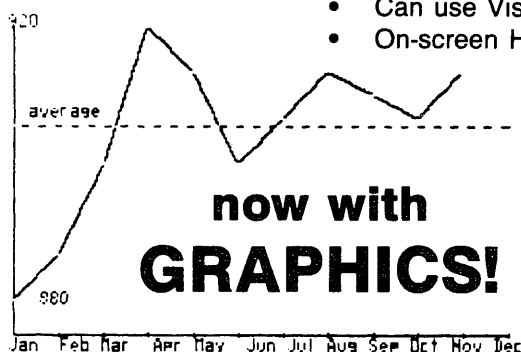
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